

Derek S. Koonce

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Seasoned market development manager and creative electronic design engineer with 12 years of semiconductor experience and over 25 years of electronic design.

Market development manager / Chief Technical Officer

- Searched for funding for a start-up fab-less semiconductor company.
- Managed \$140M business unit and interfaced with customers on an international scale.
- Generated over 100 new products for new design wins.

Visionary

- Created unique drop-in solutions for aging military electronics.
- Created new IC concepts for Power-over-Ethernet.
- Created new IC concepts for off-line ac-dc power conversion.
- Circuit design for ease of customization for future use and modifications.

Creative design engineer with problem solving focus

- Created PWM optimized P- channel devices for battery applications.
- Developed numerous prototype systems from instrumentation to power systems.

Communication oriented

- Wrote and presented technical papers for major electronic conferences.
- Wrote articles for electronic technical magazines.

Accreditations

Registered Engineer in Training, State of California
California State University, Northridge – BSEE 1988

Professional Experience

Aerometals, El Dorado Hills, CA

May 2007 – Present

Electronic Designer

- Provided drop-in replacement solutions for aging military equipment.
- Supported manufacturing start-up for large-aircraft strain gage production.

DDK Interactive Consulting Services, Roseville, CA

April 2004 – Present

Owner / Consultant / Technical Writer

- Wrote technical electronic papers for semiconductor companies.
- Provided Market Development guidance.
- Provided electronic and mechanical engineering design services.

Aerojet, Folsom, CA

March 2007 – May 2007

Electronic Engineer

- Developed drive electronics for NASA's Orion LAS attitude control.
- Electrical-Electronics Parts List Management and lightning assessment/vulnerability study.

Monolithic Microsystems, Roseville, CA

April 2006 – March 2007

Business Development / Applications Manager

- Worked on business plan development and raising funds for start-up semiconductor company.

JSI Microelectronics, McClellan, CA

Feb 2004 – April 2006

Applications Manager, Standard Product Group / Market Development

- Developed IC concepts for Power-over-Ethernet and off-line power supplies.
- Researched market potential and business viability of above products.
- Attended IEEE 802.3 Standards Committee meeting in support of PoE.

Vishay-Siliconix

May 2000 – Feb 2004

Power MOSFET Market Development Manager, Semiconductor Segment, '03 – '04

- Drove reference design wins are multiple semiconductor companies.
- Managed \$140M business unit.
- Developed 100 new product and packaging ideas, and PWM P- product line.
- Wrote technical articles and delivered presentations to major conferences.

Power MOSFET Market Development Manager, Computer Segment, '00 – '02

- Maintained segment's \$140M world-wide business during the high-tech slump.
- Generated over 100 new MOSFET product ideas based on market and customer requirements.

Supertex, Inc.

October 1995 – May 2000

Senior Applications Engineer

- Provided applications support for PWM ICs and custom high voltage ICs.
- Worked with customer to develop requirements for high-speed printer IC.
- Worked with domestic and international plasma TV manufacturers IC usage.
- Created innovative method to drive several electroluminescent lamps with single IC product.
- Wrote specifications and application notes for IC products.

Loral Electro-Optical Systems

January 1988 – October 1995

Electrical Development Engineer

- Developed, field tested, and moved to manufacturing a surface vehicle countermeasure (CM) system.
- Redesigned lamp driver magnetics for aircraft CM systems.
- Helped develop switched mode power supply for aircraft CM system with optical output shaping method.
- Developed and tested several prototype CM systems for use against advanced weapon systems.